# Durga Keerthi Mandarapu

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# Summary\_

My current research involves developing abstractions of Ray Tracing hardware to reduce and accelerate a non-ray tracing problem. I study the applications that use trees as indexing structures and map them to the tree operations performed by the Ray Tracing cores. My broad interests are parallel systems, high-performance computing, databases, and compilers

# Education

## Ph.D. in Computer Science

Advisor: Prof. Milind Kulkarni, Purdue University (GPA:3.95/4.00)

Bachelors(Honors) in Computer Science and Engineering with Minor in Economics

INDIAN INSTITUTE OF TECHNOLOGY, HYDERABAD (GPA:8.78/10.00)

# Publications\*\*

**Durga Mandarapu**, Nicholas James, and Milind Kulkarni. **Mochi: Fast & Exact Collision Detection.** Under submission to Eurographics 2024.

Durga Mandarapu, Vani Nagarajan, and Milind Kulkarni. Generalized Neighbor Search using Commodity Hardware Acceleration. In PPoPP 2024 review.

Vani Nagarajan, **Durga Mandarapu**, and Milind Kulkarni. 2023. **RT-kNNS Unbound: Using RT Cores to Accelerate Unrestricted Neighbor Search.** In Proceedings of the 37th International Conference on Supercomputing (ICS '23). Association for Computing Machinery, New York, NY, USA, 289–300. [link]

Arsekar, R., Mandarapu, D.K., Rao, M.V.P. (2017). EpiStrat: A Tool for Comparing Strategies for Tackling Urban Epidemic Outbreaks. In: Chen, H., Zeng, D., Karahanna, E., Bardhan, I. (eds) Smart Health. ICSH 2017. Lecture Notes in Computer Science(), vol 10347. Springer, Cham.

# Internships

# Distributed Random Walks

Software Engineering Intern, Katana Graph

• Worked on developing a scalable uniform random walks application to overlap communication and computation costs on distributed graphs using the Katana interface.

#### **Betweenness Centrality for Streaming Graphs**

MITACS INTERNSHIP UNDER THE GUIDANCE OF PROF. KEVAL VORA, SIMON FRASER UNIVERSITY

• Developed a parallel incremental algorithm that processes non-monotonous dynamic edge updates to compute a betweenness centrality measure of all the vertices in a streaming graph.

## **Credit Networks for better Payment Systems**

PURE INTERNSHIP UNDER THE GUIDANCE OF PROF. ANIKET KATE, PURDUE UNIVERSITY

• Developed a credit network using smart contracts in Ethereum that allow payments across different currencies without introducing a new crypto-currency and at a lowered account-creation, direct-payment, and currency transaction costs.

## Strategy Selection in Epidemic Management using Agent-Based Modeling

Guide: Prof. M. V. Panduranga Rao, IIT Hyderabad

• Developed a tool that performs a scalable simulation of an epidemic that uses agent-based modeling of individuals to understand and predict how the disease could spread in an urban slum community.

# **Selected Research Projects**

## Collision Detection with Ray Tracing Cores (Under Submission)

Advisor: Prof. Milind Kulkarni, Purdue University.

• Devised object-object intersection test on RT interface by tracing the edges of the object's bounding volume.

• Devised 2D ray-triangle intersection test on RT interface that performs only 3D ray-triangle intersection by introducing proxy triangles.

# Neighbor Search on Ray Tracing Cores (In review)

Advisor: Prof. Milind Kulkarni, Purdue University.

- Implemented non- $L^2$  distances on ray tracing cores that order objects on  $L^2$  distance, by formulating two generic distance computations.
- Working on extending the search to data points from higher dimensions(1e3), although ray tracing cores only expose 3D indexing structures.

# W.Lafayette, U.S.A. Aug. 2019 - May. 2025\*

Hyderabad, India Jul. 2015 - Apr. 2019

## W.Lafayette, U.S.A.

Vancouver, Canada

Summer 2018

Austin, U.S.A.

Summer 2022

Summer 2019

Hyderabad, India

Fall 2016 - Spring 2017

W. Lafayette, U.S.A.

W. Lafayette, U.S.A.

Summer 2023\*

Fall 2022\*

GUIDE: PROF. WALID AREF, PROF. JIANGUO WANG, PURDUE UNIVERSITY. Implemented 2-Phase-Locking (wait-die, no-wait), Optimistic Currency Control (Forward-OCC, Backward-OCC), and Multi-Version Concurrency

Control (multi-version timestamp ordering) protocols using RDMA to process transactions on a 5-node cluster.

## Algorithms for overlaying topologies in Data Center Networking

RESEARCH ASSISTANT, GUIDE: PROF. DOUGLAS COMER, PURDUE UNIVERSITY.

• Developed a heuristic that greedily selects a neighbor to map logical overlay topologies to physical underlay topologies. Runs in polynomial time which is otherwise an NP-problem.

## Support for Shuffle, Broadcast and Reduce in Serverless Computing

Guide: Prof. Pedro Fonseca, Purdue University.

• Developed a user library that handles the direct communication between the lambda functions in Serverless Computing architecture for workloads that involve communication patterns like broadcast, shuffle, and reduce.

## Parallel Sparse Matrix-Matrix Multiplication

Guide: Prof. Sathya Peri, IIT Hyderabad.

• Developed a lock-free and wait-free algorithm that uses relaxed barrier constraints to mitigate the synchronization delays between threads for making applications like sparse matrix-matrix multiplication more scalable.

#### **Component Specific Passes on LLVM**

Guide: Ramakrishna Upadrasta, IIT Hyderabad.

• Divided a program into basic components, and the clang compiler optimization passes into sub-sequences of transform and analysis passes to show that the performance achieved by applying all the passes can also be achieved by just a sub-sequence of them in a shorter time.

#### **Optimistic Algorithms for Distributed Transactional Memory**

Guide: Prof. Sathya Peri, IIT Hyderabad.

• Developed a library that uses a distributed basic timestamp ordering algorithm that can be plugged in to read and write shared objects in a transactional memory. Optimized the number of messages exchanged to remove redundant notifications.

# Positions of Responsibility \_

Graduate Research Assistant, Prof. Milind Kulkarni, Purdue University

Graduate Teaching Assistant, Data Structures, Purdue University

#### Undergraduate Teaching Assistant, IIT Hyderabad

Operating Systems (Fall 2018, Spring 2019), Database Systems (Spring 2019), Data Structures (Fall 2017), Introduction to Programming (Fall 2017)

Lit-soc (Literary Society) Coordinator, National Service Scheme, IIT Hyderabad chapter Fall 2017 - Spring 2018

- Organized weekly sessions for students on Computers, English, Mathematics, and Science at local government schools.
- Developed a database of presentations on the topics from the high school textbooks, with the help of the IITH student community

# **Grants & Awards**

- PLDI, ACM SIGPLAN Conference on Programming Language Design and Implementation, Travel Grant 2023
- SOCC, ACM Symposium on Cloud Computing, Travel Grant 2019
- 2019 MITACS, scholarship to intern in Canada
- JENESYS, Indian cultural ambassador to Japan, funded by Embassy of Japan 2018

\* marked refer to continuing in present timeline.

\*\* published, in review and unpublished conference papers.

Fall 2018

Hyderabad, India

Fall '21 - Fall '23\*

Fall '19 - Summer '21

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W. Lafayette, U.S.A.

Fall 2020

W. Lafayette, Indiana

Summer 2020

W. Lafayette, Indiana

Hyderabad, India Fall 2018 - Spring 2019

Hyderabad, India Spring 2019